

RESULTS

Branch: DIGITAL SYSTEM & COMPUTER ELECTRONICS

Course/Sem: M.Tech. II Semester Supplementary Examinations (MLR-15)

Held in: JANUARY 2018

Roll No SGPA CGPA

ACA	CFAA	DSPA	EN	ES Lab	LPVD	S-II	WLP			
Advanced Computer Architecture	CPLD and FPGA Architectures and Applications	Digital Signal Processors and Architectures	Embedded Networking	Embedded Systems Laboratory	Low Power VLSI Design	Seminar-II	Wireless LANs and PANs			
Grade	Grade	Grade	Grade	Grade	Grade	Grade	Grade			
15R21D0604	4.77	6.02	B	B	B+	B+	F(Ab)	A+	F	F

Note: In case of any discrepancy, the concerned student may contact the CE immediately