

13-07-2017

## **ADVERTISEMENT**

**Applications are invited for the post of Junior Research Fellow for a SERB project at the Department of Electronics & Communication Engineering, MLR Institute of Technology [ECR/2017/00637].**

**Project Title:** Design and Development of an On-Chip Heterogeneous Multi-Core Computing System for Secure Multimedia Applications.

**Qualification:** B. Tech / B.E in Electronics & Communication, ECE/CSE, M.E/M.Tech in VLSI or Embedded System Design related field, GATE/NET Qualified.

**Number of Position:** One

**Principal Investigator:** Dr. Mahendra Vucha, Department of Electronics & Communication Engineering, MLR Institute of Technology.

**Duration:** 36 Months

**Fellowship:** Rs. 25,000/Month.

Interested candidates can send their CV and latest passport size photo with the subject line of “JRF–SERB-ECR/ Dr. Mahendra V ” by post to Dr Mehendra V, Professor, Dept. of Electronics & Communication,MLR Institute of Technology, Laxman Reddy Avenue, Dundigal,Quthbullapur Mandal, Hyderabad-043,Telangana, India or by email to: [mahendra.v@mlrinstitutions.ac.in](mailto:mahendra.v@mlrinstitutions.ac.in) with a subject line of “JRF–SERB-ECR”.

- No TA and DA will be paid for appearing the interview
- Shortlisted candidates will be called for an interview at a later date which will be intimated by email.
- Last date for receipt of Application: 20-07-2017