

ELECTRONIC CIRCUITS ANALYSIS LAB

II B-Tech-II Semester

LT P C

Course Code: A3EC14

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Course Objectives

1. Design and simulate various BJT and FET amplifiers.
2. Design and simulate various BJT Feedback amplifiers.
3. Design and simulate various BJT Oscillators.
4. Design and simulate various power amplifiers and tuned amplifiers.

Course Outcomes

After going through this course the student will be able to

1. Apply the concepts of amplifiers in the design of Public Addressing System
2. Design various Audio frequency Oscillators.
3. Design Amplifier using feedback concepts.
4. Design various applications using amplifier.

LIST OF EXPERIMENTS: (Minimum 12 Experiments to be conducted)

PART – A

DESIGN AND SIMULATION USING MULTISIM

1. Common Emitter Amplifier.
2. Common Collector Amplifier.
3. Voltage Series Feedback Amplifier.
4. Current Shunt Feedback Amplifier.
5. Two Stage RC Coupled Amplifier.
6. Darlington Pair Configuration.
7. Class A Power Amplifier (Transformer less).
8. Class B Complementary Symmetry Push Pull Amplifier.

PART – B

TESTING IN THE HARDWARE LABORATORY:

a) Any Three circuits simulated in Simulation laboratory b) Any Three of the following

1. Common Source Amplifier.
2. Hartley Oscillator.
3. Colpitt's Oscillator.
4. Class A Power Amplifier (with Transformer Load)
5. Class B Power Amplifier.
6. Single Tuned Voltage Amplifier.
7. RC Phase Shift Oscillator.
8. Wien Bridge Oscillator.